



## *Input Terminal Pin Assignment*

<b>Pin no</b>	<b>Symbol</b>	<b>Description</b>	<b>Remark</b>
1	VBL-	Backlight LED ground	-
2	VBL-	Backlight LED ground	-
3	VBL+	Backlight LED power	-
4	VBL+	Backlight LED power	-
5	Y1	Touch panel TOP	-
6	X1	Touch panel RIGHT	-
7	NC	NO CONNECTION	-
8	/RESET	Hardware reset	-
9	SPENA	SPI Interface Data Enable Signal	-
10	SPCLK	SPI Interface Data Clock	-
11	SPDAT	SPI Interface Data	-
12	B0	Blue data bit 0	-
13	B1	Blue data bit 1	-
14	B2	Blue data bit 2	-
15	B3	Blue data bit 3	-
16	B4	Blue data bit 4	-
17	B5	Blue data bit 5	-
18	B6	Blue data bit 6	-
19	B7	Blue data bit 7	-
20	G0	Green data bit 0	-
21	G1	Green data bit 1	-
22	G2	Green data bit 2	-
23	G3	Green data bit 3	-
24	G4	Green data bit 4	-
25	G5	Green data bit 5	-
26	G6	Green data bit 6	-
27	G7	Green data bit 7	-
28	R0	Red data bit 0	-
29	R1	Red data bit 1	-
30	R2	Red data bit 2	-
31	R3	Red data bit 3	-
32	R4	Red data bit 4	-
33	R5	Red data bit 5	-

<b>Pin no</b>	<b>Symbol</b>	<b>Description</b>	<b>Remark</b>
34	R6	Red data bit 6	-
35	R7	Red data bit 7	-
36	HSYNC	Horizontal sync input	-
37	VSYNC	Vertical sync input	-
38	DOTCLK	Dot data clock	-
39	NC	NO CONNECTION	-
40	NC	NO CONNECTION	-
41	VDDIO	Digital power	-
42	VDDIO	Digital power	-
43	Y2	Touch panel BOTTOM	-
44	X2	Touch panel LEFT	-
45	NC	NO CONNECTION	-
46	NC	NO CONNECTION	-
47	NC	NO CONNECTION	-
48	NC	NO CONNECTION	-
49	NC	NO CONNECTION	-
50	NC	NO CONNECTION	-
51	NC	NO CONNECTION	-
52	ENB	Data enable control	-
53	VSS	Ground	-
54	VSS	Ground	-